

A	B	C
0	0	1
0	1	0
1	0	0
1	1	0

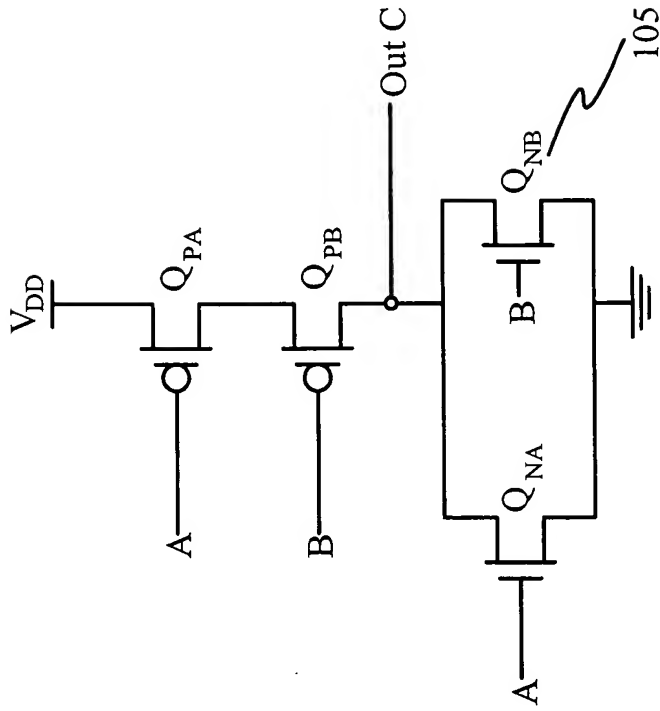


Fig. 1

D	E	F
0	0	1
0	1	1
1	0	1
1	1	0

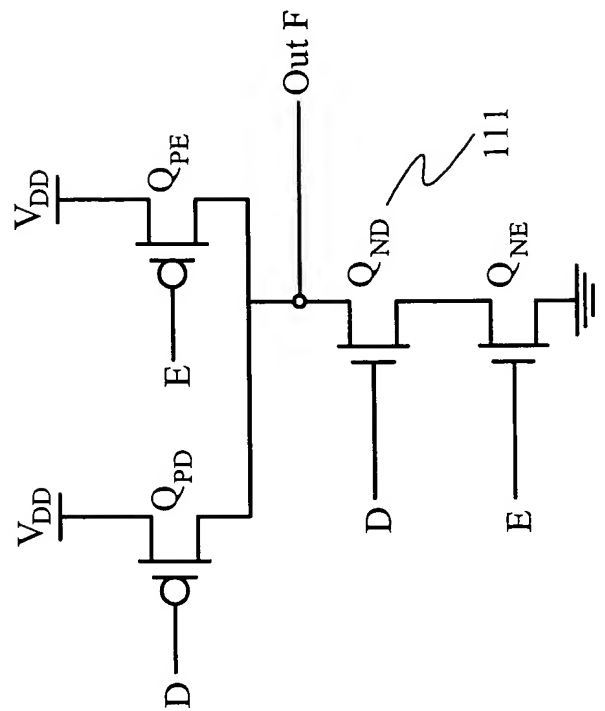


Fig. 2

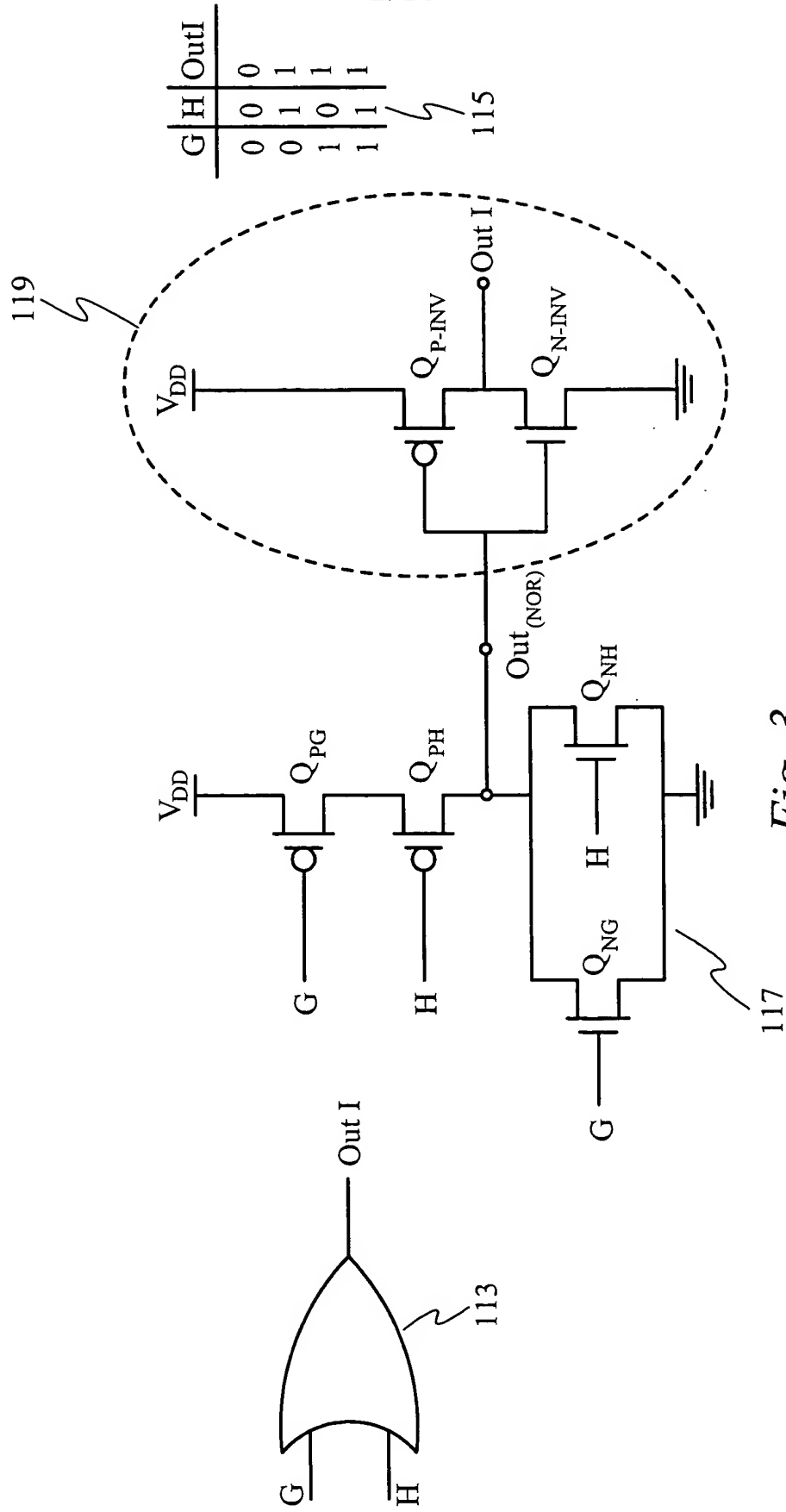


Fig. 3

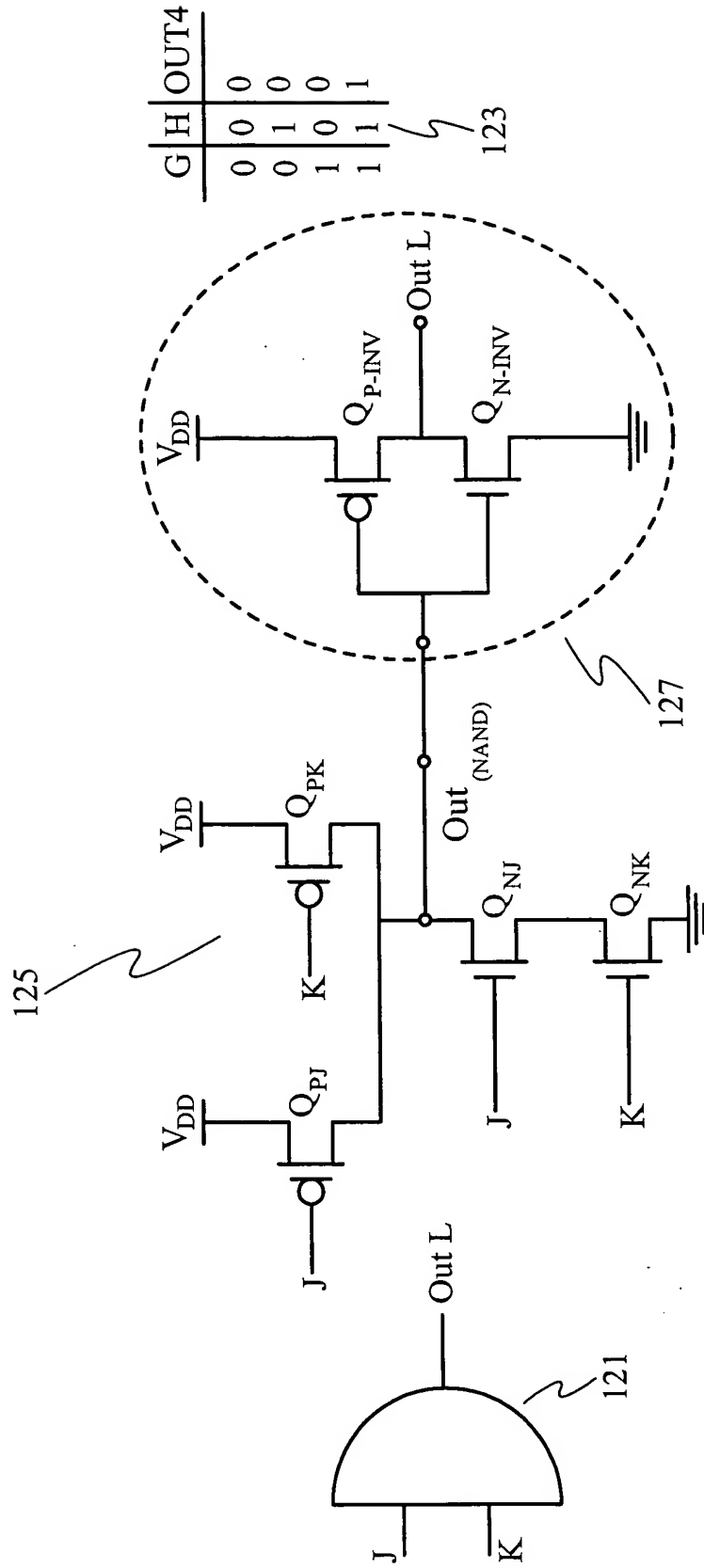
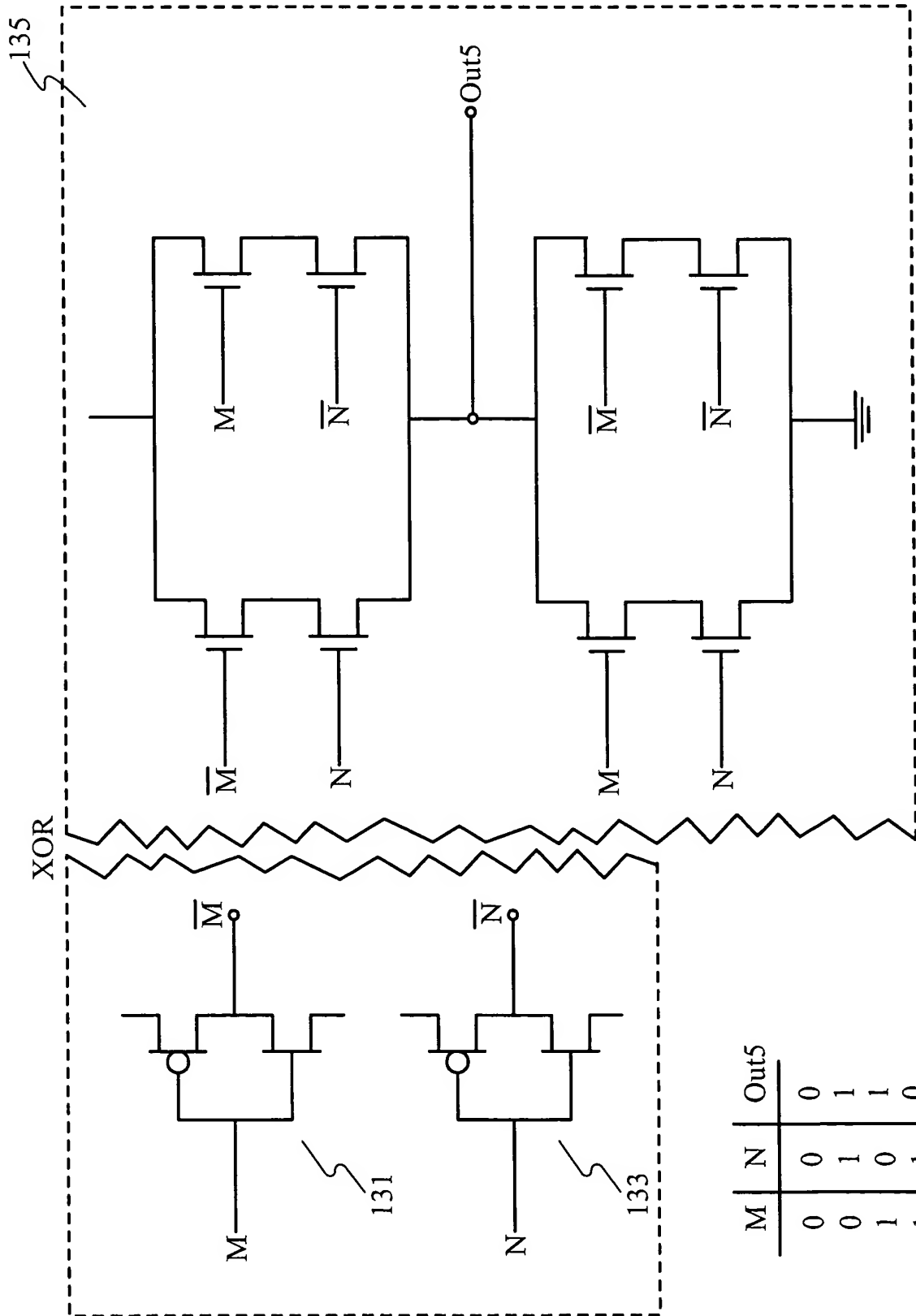


Fig. 4



M	N	Out5
0	0	0
0	1	1
1	0	1
1	1	0

Fig. 5

137

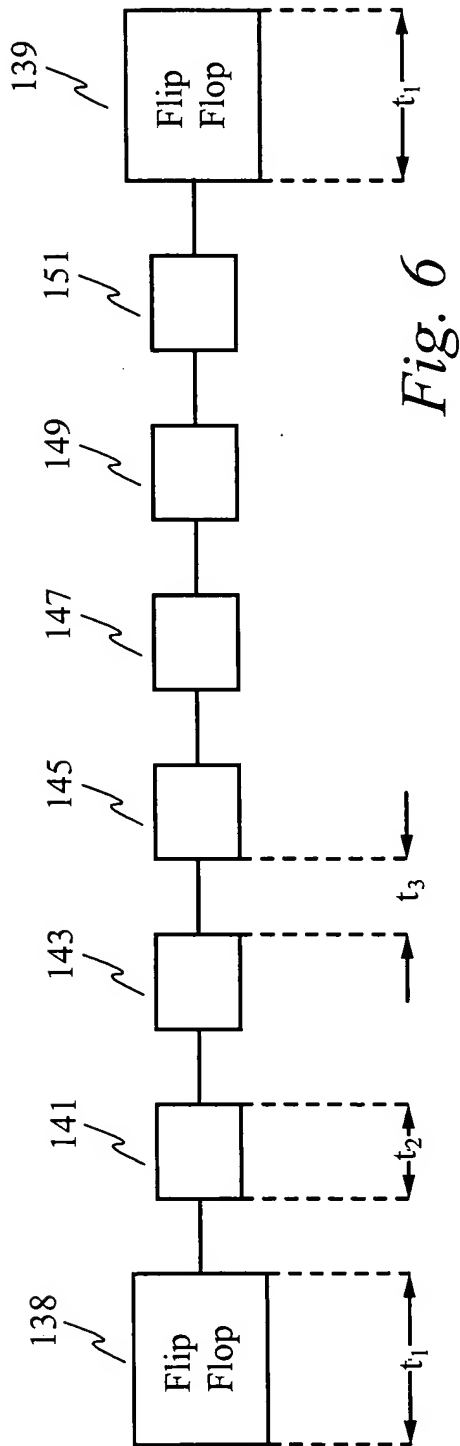


Fig. 6

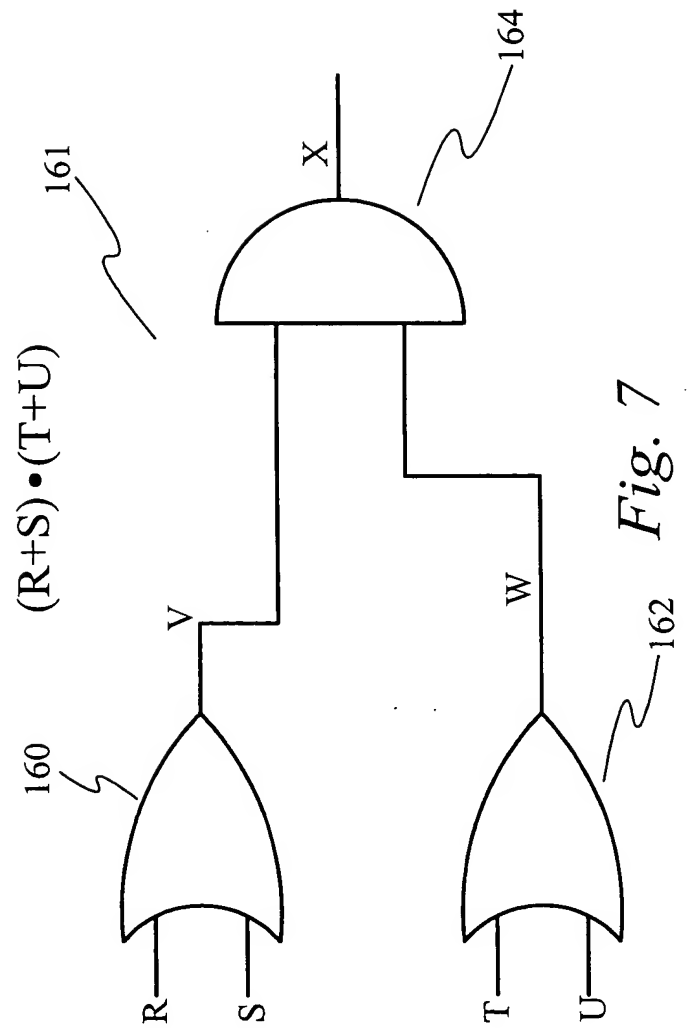


Fig. 7

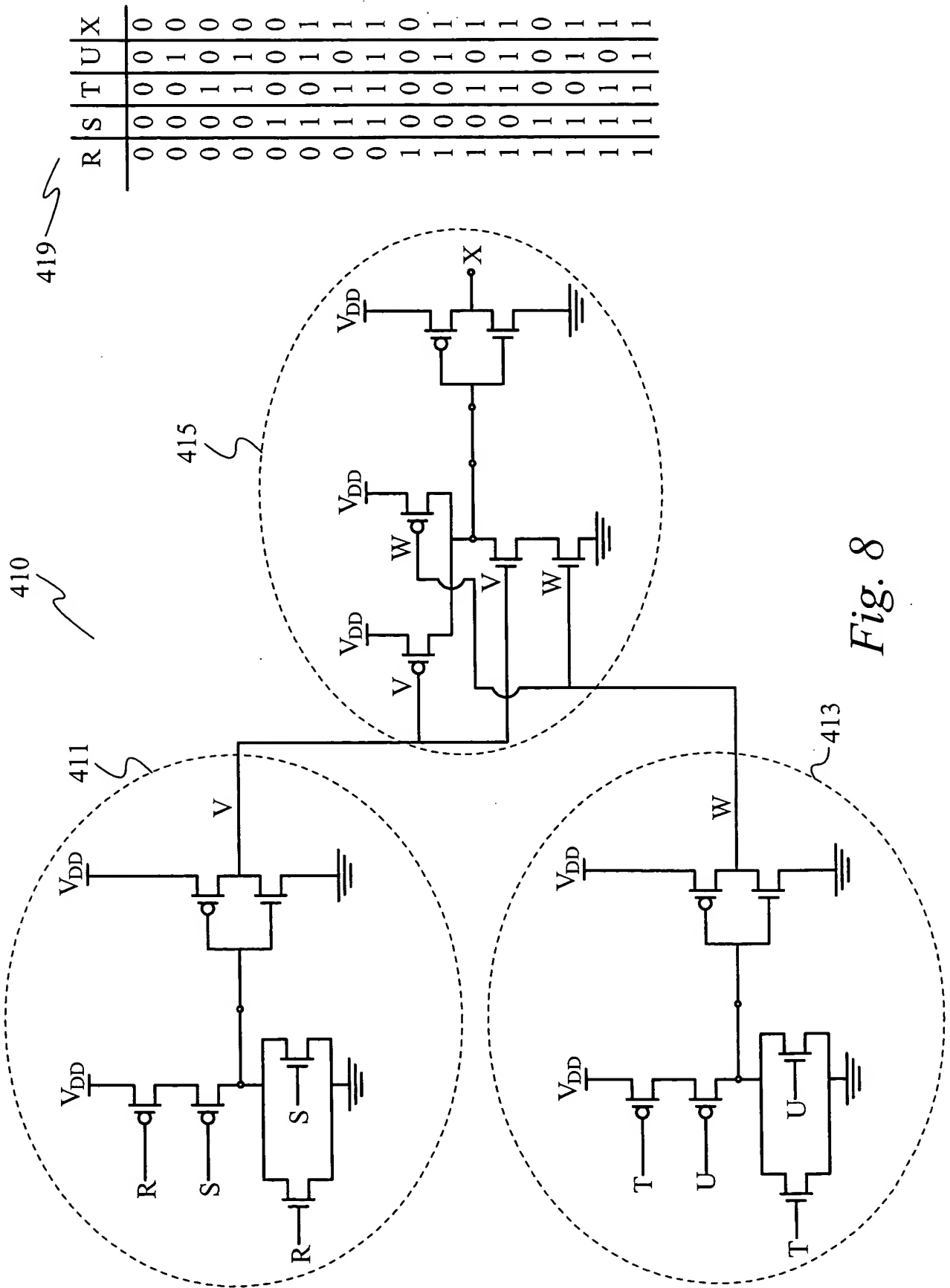


Fig. 8

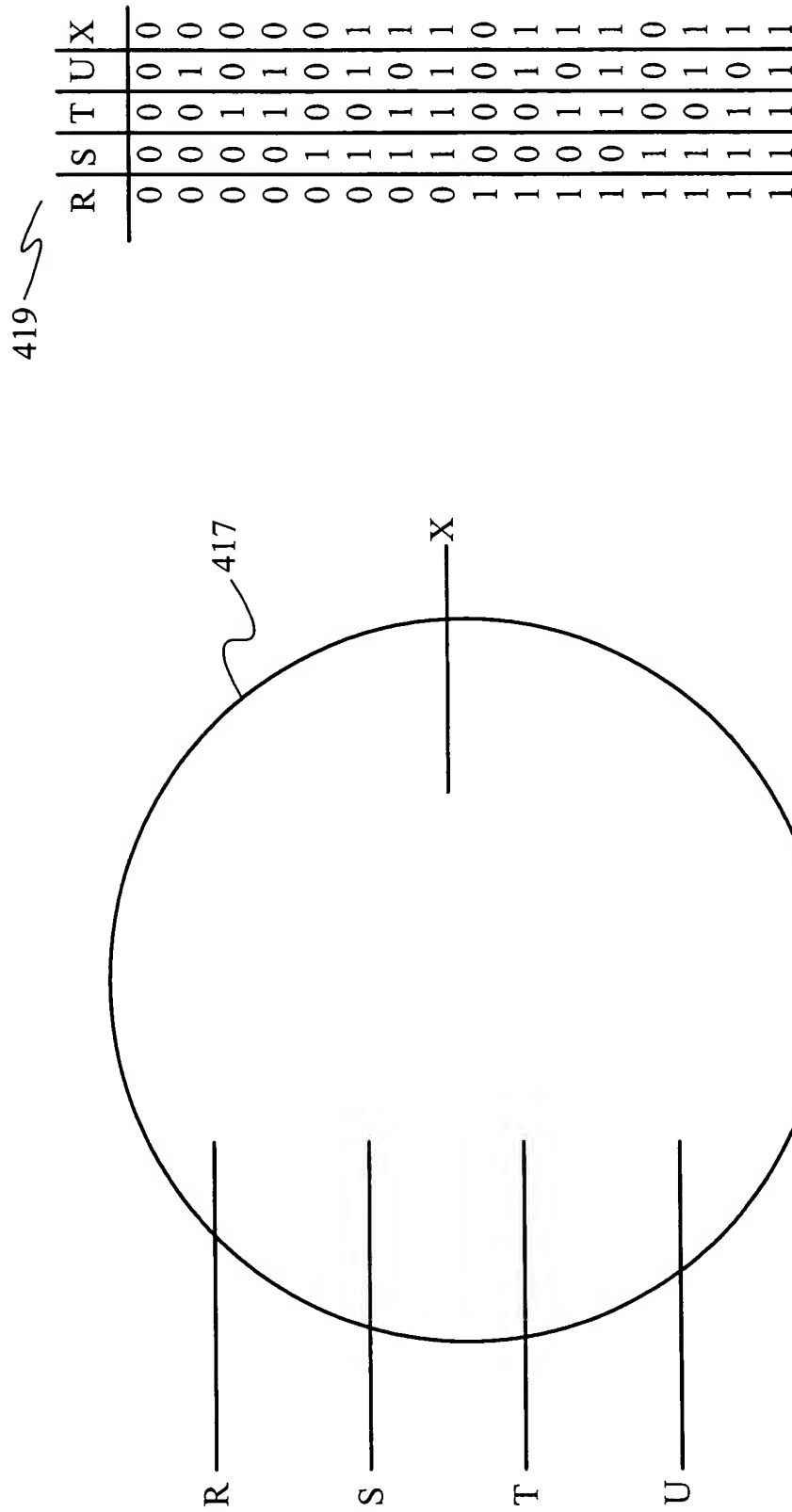


Fig. 9

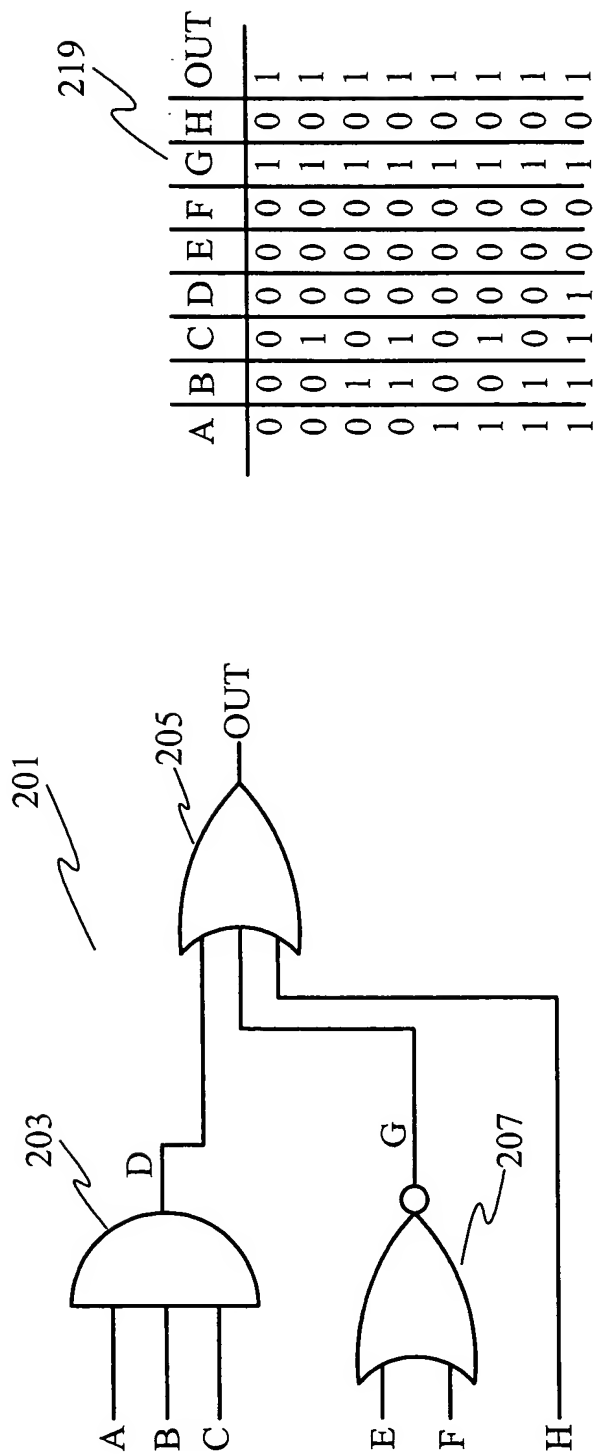


Fig. 10

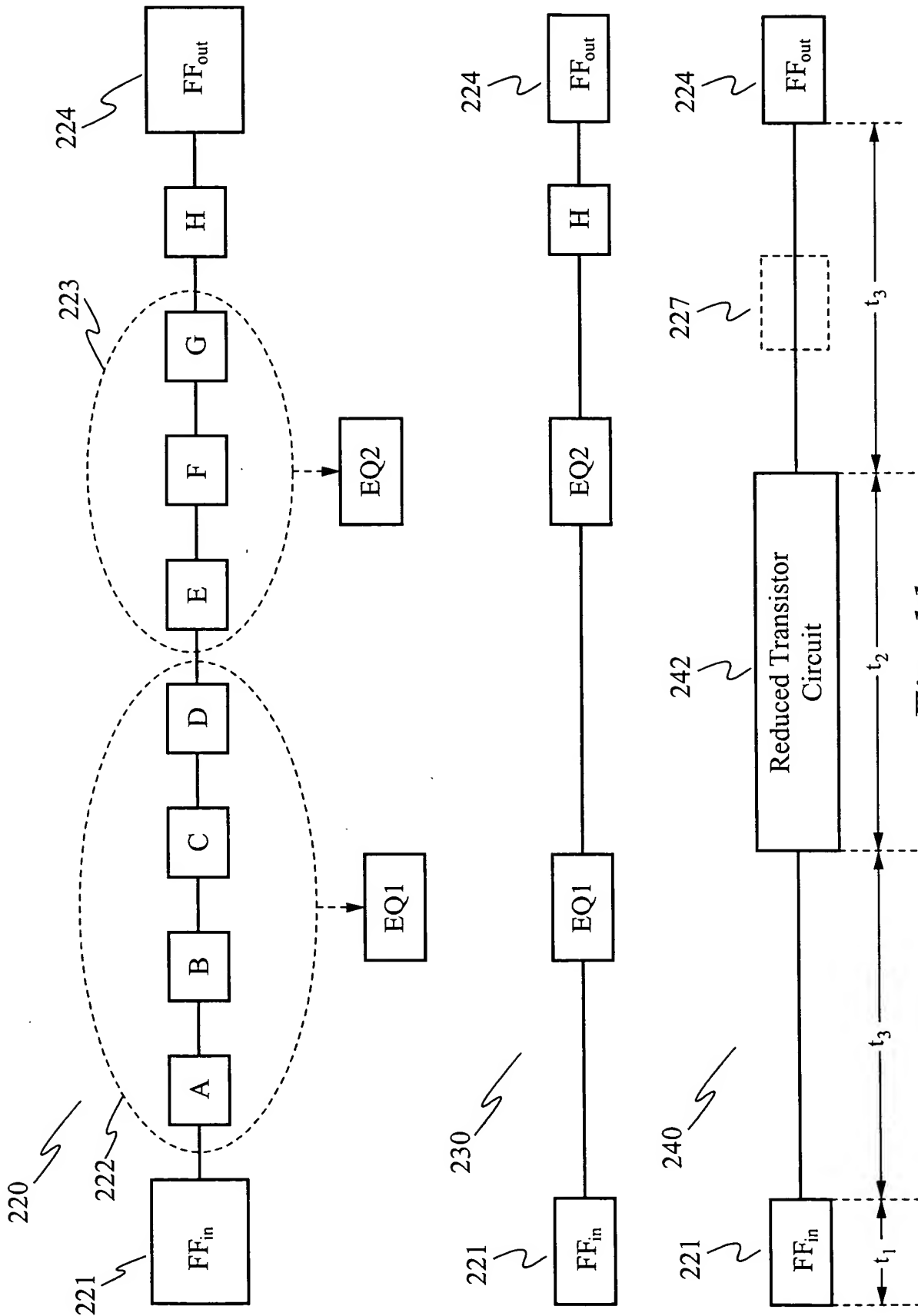
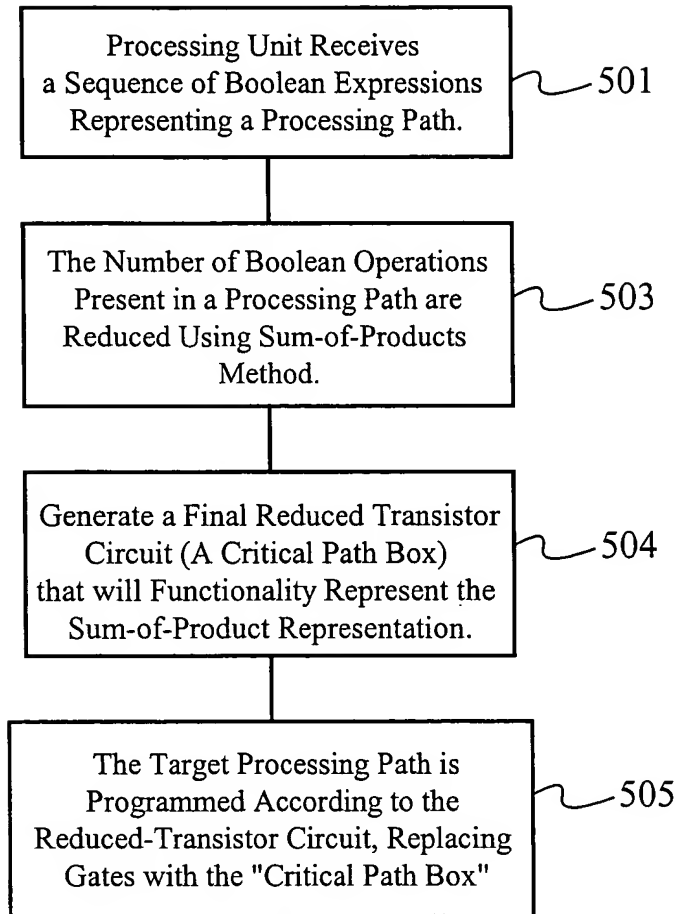
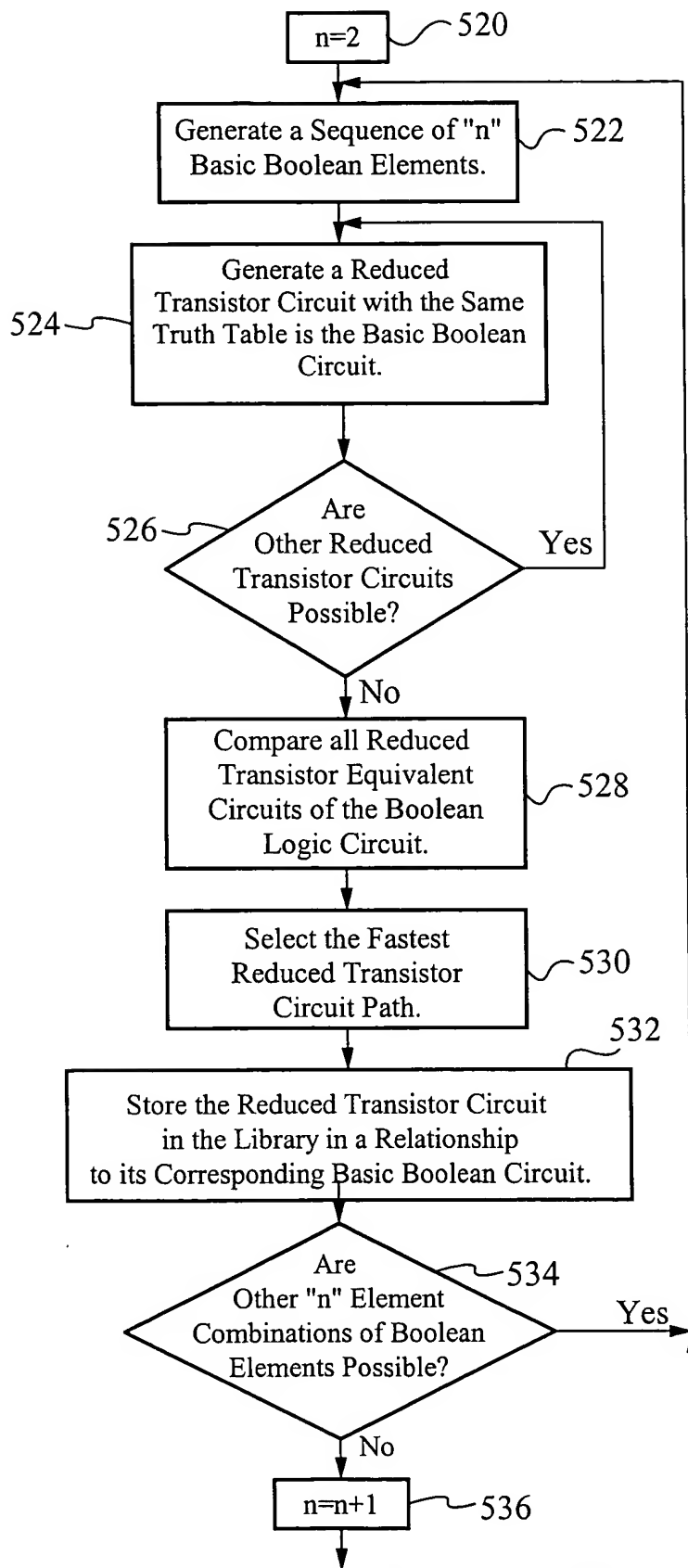


Fig. 11

*Fig. 12*

*Fig. 13*

LIBRARY

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430

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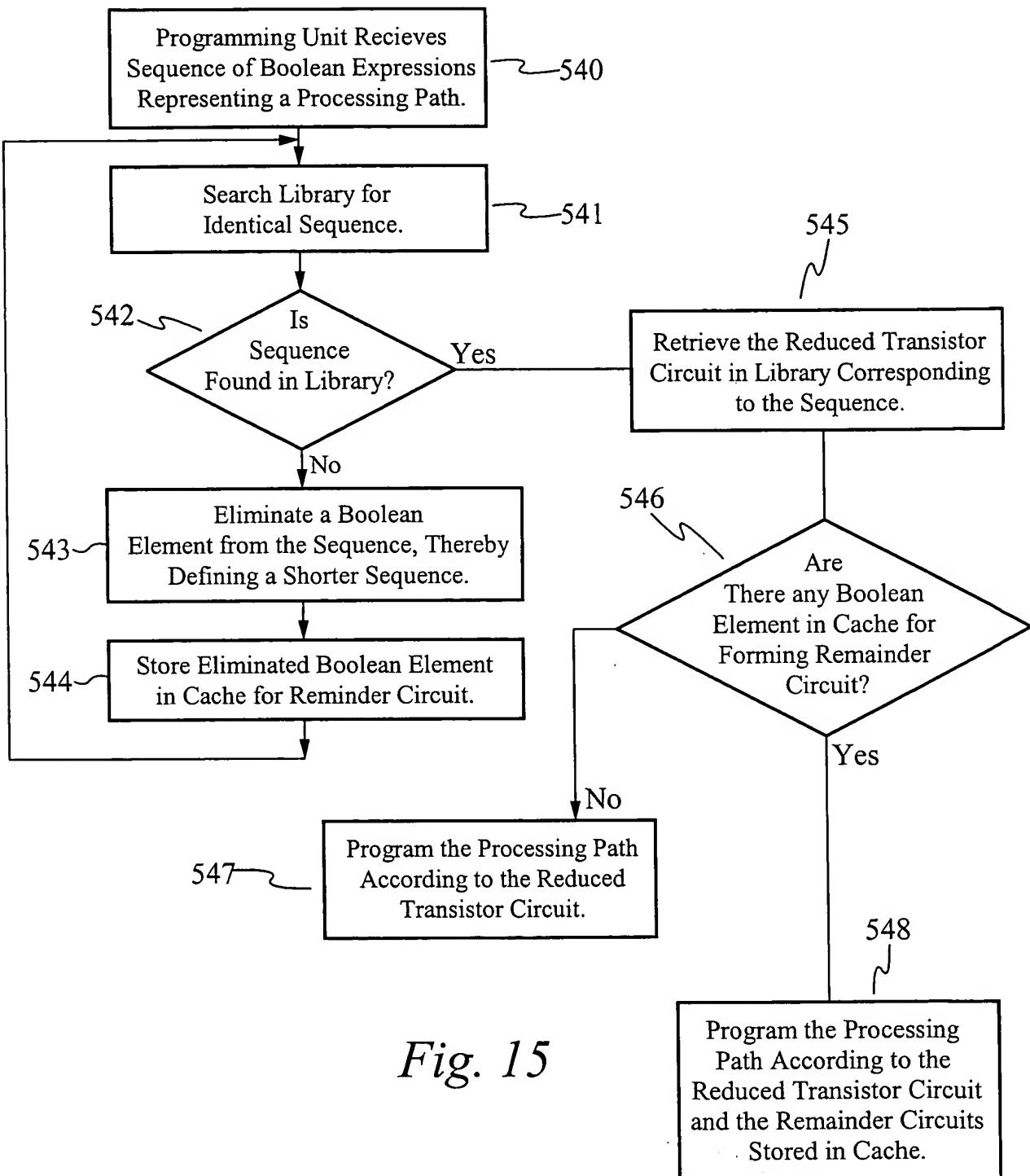
Number of Operators	Boolean Sequence	Reduced-Transistor Substitute Circuit
1	AND (A*B)	Code Defining Reduced-Transistor Equivalent Circuit
1	OR (A+B)	Code Defining Reduced-Transistor Equivalent Circuit
1	XOR	Code Defining Reduced-Transistor Equivalent Circuit
1	NOT	Code Defining Reduced-Transistor Equivalent Circuit
2		
2	(A+B) + C	Code Defining Reduced-Transistor Equivalent Circuit
2	(A+B) * C	Code Defining Reduced-Transistor Equivalent Circuit
2		
•		
•		
•		
3		
3	(A+B) + (C+D)	Code Defining Reduced-Transistor Equivalent Circuit
3	(A+B) * (C+D)	Code Defining Reduced-Transistor Equivalent Circuit
3	•	Code Defining Reduced-Transistor Equivalent Circuit
•	•	Code Defining Reduced-Transistor Equivalent Circuit
•	•	Code Defining Reduced-Transistor Equivalent Circuit
4	•	Code Defining Reduced-Transistor Equivalent Circuit
4		

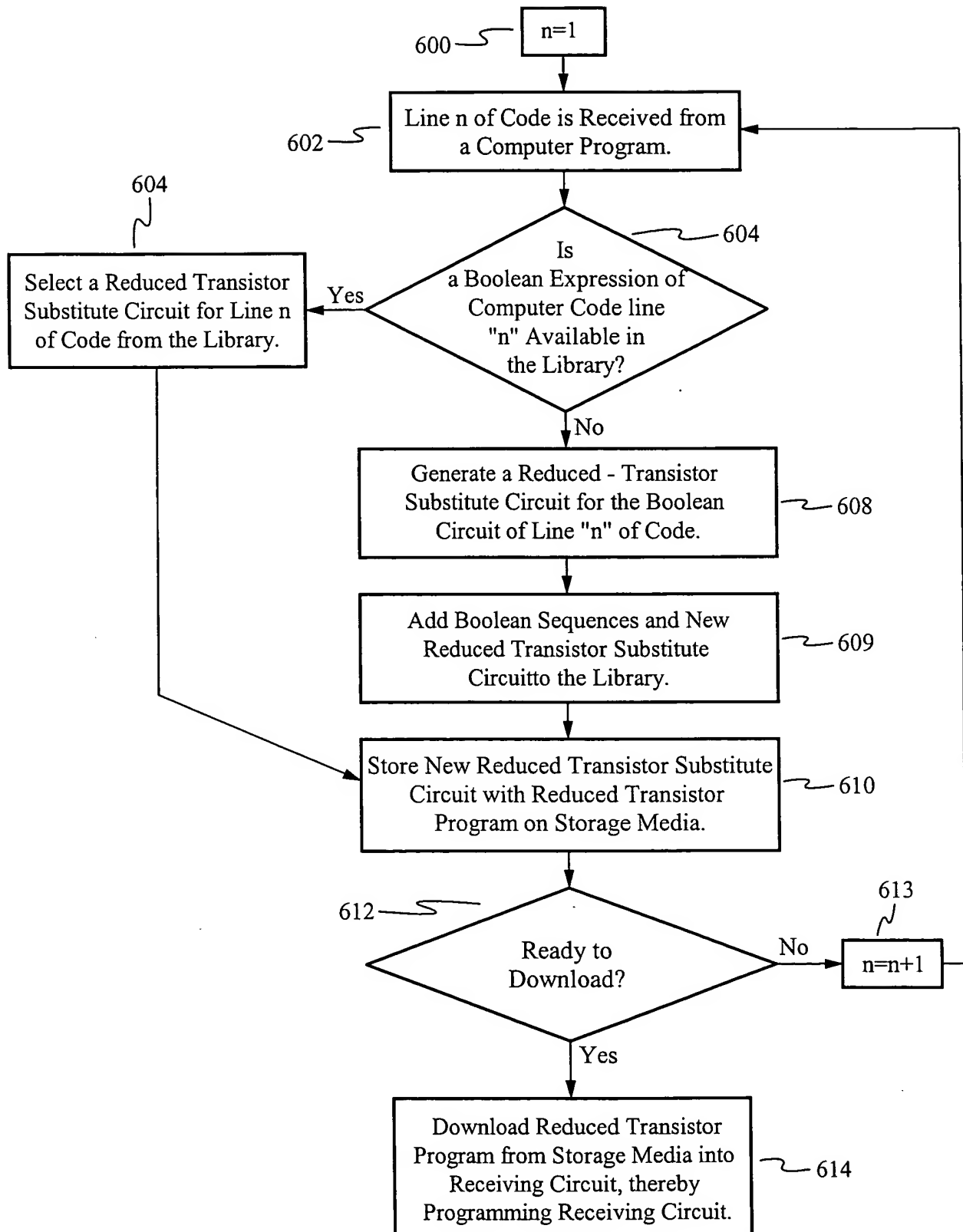
Search
EngineProgramming
UnitIntegrated
Circuit

441

443

Fig. 14

*Fig. 15*

*Fig. 16*

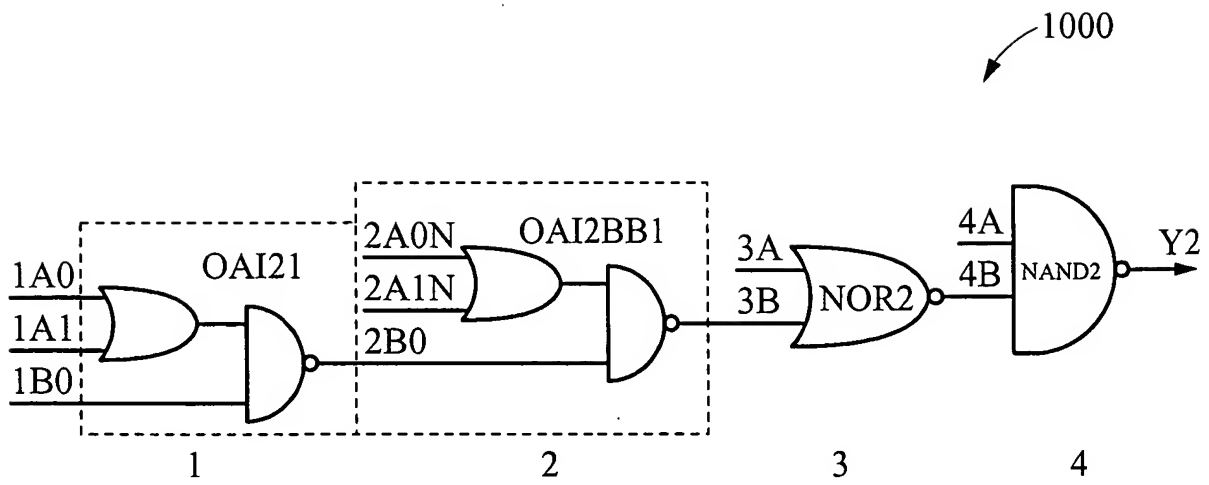


Fig. 17

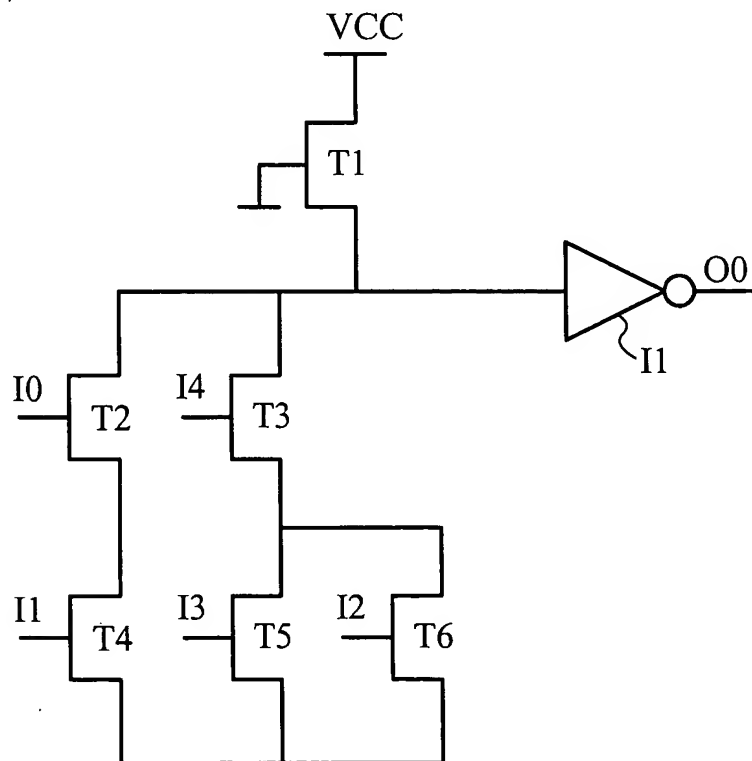
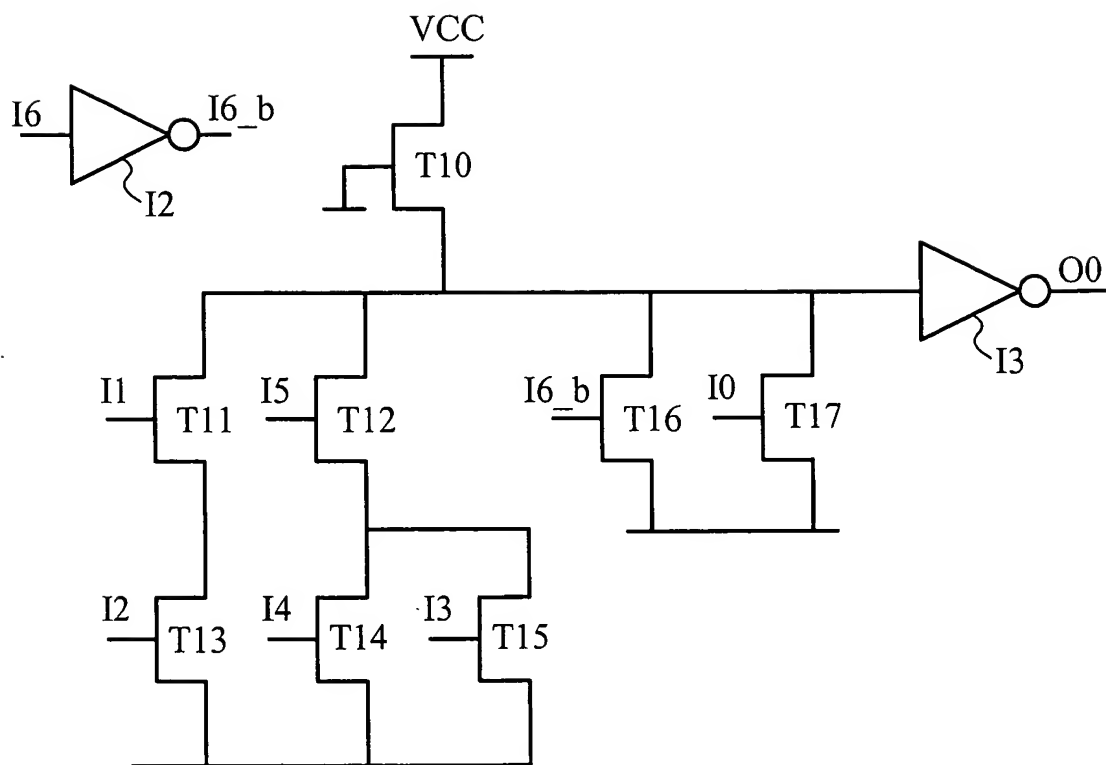


Fig. 18

*Fig. 19*